

Join us for the
13th Annual IEEE Hot Interconnects
**Symposium on High
Performance Interconnects**

The international forum where the high-performance computing
and high-speed networking communities meet.

Stanford University
William R. Hewlett Teaching Center
at the Science & Engineering Quad(TCSEQ)

AUGUST 17 - 19, 2005

Keynotes both days: Raj Yavatkar, Intel and Manesh Gupta, IBM

Great tutorials on Wednesday!

T1 Using the Open Network Lab

T2 Quality of Service in Global Grid Computing

T3 Internet Infrastructure Security

T4 High-Speed Networking: A Systematic Approach to High Bandwidth Low-Latency Communications

2 days of Session Talks on Thursday & Friday

Session 1: Networks for Large-Scale Parallel Computers

Session 2: Performance Evaluation of High-Performance Interconnects

Session 3: Network Processors and Associative Technology

POSTER SESSION during Reception Dinner

Session 4: High Performance Routers

Session 5: TCP and Switch Architecture

Controversial Panel Discussions both days:

- Impact of Grid Computing on Network Operators and HW Vendors
- EtherNET vs. EtherNOT

**Great food. Great people. Great talks.
We hope to see you there.**

Many thanks to our Patrons:
Xilinx | Intel | Global Velocity | PathScale

<http://www.hoti.org>